REMARKS

Claims 1-13 are pending in the application. Claims 1-13 are rejected.

Claims 1-3, 5, 6, 8, 10, 12, and 13 were rejected under 35 USC102(b) as being anticipated by Holmes. Claims 4, 9, and 11 were rejected under 35 USC103(a) as being unpatentable over Holmes in view of Ishibashi. Claim 7 was rejected under 35 USC103(a) as being unpatentable over Holmes in view of Applicant's Admitted Prior Art. Claim 1 includes "... repeating the above steps N times during a frame time cycle ...". Claim 5 includes "…repeating the above steps N times during a frame time cycle, … and performing digital integration in the digital memory device by cumulatively combining the digital signal of a present step with the digital signal of previous steps." Claim 10 includes "… wherein the digital memory device cumulatively combines an output from each pixel with an output from a previous readout of each pixel each time the array of image sensing pixels is readout during a frame time cycle." The references of record do not show, teach, or suggest the above recited limitations of claims 1, 5, and 10. The Holmes reference does not teach repeating the charge integration steps multiple times in a frame time. The Holmes reference teaches taking the charge integration from multiple frames and combining the data by shifting the pixel data to match up with the location of the object in the first step, then combining the data. See column 5, lines 41-42. Claims 2-4 depend from claim 1. Claims 6-9 depend from claim 5. Claims 11-13 depend from claim 10. Therefore, claims 1-13 are believed to be allowable over the references of record.

It is believed that the above remarks are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Should the Examiner have further inquiry concerning these matters, please contact the below named attorney for Applicant.

Respectfully submitted,

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